## **CURRICULUM VITAE (CV)**

## **Personal detail:**

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| University            |  |  |  |
|-----------------------|--|--|--|
| Qualification         | a.                                       | B.S.: URMIA University, Electrical engineering Department, URMIA, Iran.<br>Bachelor of Science in Electrical engineering faculty, 2000 |  |
|                       |  | B.S thesis topic: "DC-DC converter based on Fuzzy Logic"   |  |
|                       | b.                                       | M.S.: Azad University, Electrical Engineering department, Tabriz, Iran.<br>Master of Science in Electrical Engineering ,2008           |  |
|                       |  | M.S thesis topic: "A 10 Bit Low Power Successive Approximation Analog to Digital Converter"  |  |
|                       | c.                                       | PH.D: University Technology Malaysia (UTM), Faculty of Electrical Engineering, Johor, Malaysia, 2013                                   |  |
|                       |  | PH.D Thesis Topic: "Radio Frequency Circuit Design Based On Variable Resistance Active Inductor".                                      |  |
| HONORS/<br>AWARDS     | My honor at the high school and College: |  |  |
|                       | •  | I was <b>top</b> student at High School<br>I was <b>top</b> student at college always  |  |
| RELATED<br>EXPERIENCE |  | 1- Designing "Low power, high speed comparators for ADC applications".   |  |
|                       |  | 2- Designing "Low power Analog to Digital convertor".  |  |
|                       |  | 3- Designing VCO, Mixer and LNA  |  |
|                       |  | 4- Designing "DC-DC convertor based on Fuzzy Logic".   |  |
|                       |  | 5- Designing "Switching and Regulated DC Power Supply"   |  |
|                       |  | 6- Designing electrical control system.  |  |
|                       |  | 7- Designing control system for Electrical motors.(AC and DC)  |  |

8- Designing "Inductor-less Low Noise Amplifier For RF applications".
9- Designing "New and High performance Active inductors for RF applications".
10- Designing "Differential Low Noise Amplifiers Based on Active Inductors".
11- Designing "CMOS RF Receiver Front-Ends".
12- Designing "VCO Based on Active Inductor Circuit".
13- Designing "LNA Based on Active Inductor Circuit".
14- Designing " Low Flicker Noise Mixer for Direct Conversion Receivers".
15- .....

**ADDITIONAL** I am familiar with these Softwares: Cadence, Tanner tools, Hspice, Pspice, Proteus, Orcad **SKILLS** 

## **Publications:**

Published<br/>papers1- Hojjat Babaei Kia, Abu Khari A'ain, Ian Grout, Izam Kamisian, "A<br/>Reconfigureable Low Noise Amplifier Using a Tunable Active Inductor for Multi-<br/>standard Receivers ", Circuits Syst Signal Process, springer, vol. 32 pp. 979-992, 17<br/>October 2012.

2- Hojjat Babaei Kia, Abu Khari A'ain, "A single -To-Differential LNA using Differential Active Inductor for GPS applications", Frequenz, 21 December 2012.

3- Hojjat Babaei Kia, Abu Khari A'ain, Ian Grout, "Wide Tuning-Range CMOS VCO Based on a Tunable Active Inductor" International Journal of Electronics, 20 March 2013.

4- Hojjat Babaei Kia, Abu Khari A'ain, "A Wide Tuning Range Voltage Controlled Oscillator with a High Tunable Active Inductor", Wireless Personal Communications-Springer, 24 May 2014.

5-Hojjat Babaei Kia, Abu Khari A'ain, " A High Gain And Low Flicker Noise CMOS Mixer With Low Flicker Noise Corner frequency Using Tunable Differential Active Inductor", Wireless Personal Communications, Springer, 10 July 2014.

| Sent to<br>Publications | 1- Hojjat Babaei Kia , "A High Conversion Gain And Low Flicker Noise Mixer<br>Using Noise Reduction Technique For Direct Conversion Receivers", Circuits Syst<br>Signal Process, springer  |  |
|-------------------------|--|--|
|                         | 2- Hojjat Babaei Kia ," A Noise Canceling CMOS LNA exploiting Differential Active<br>Inductor", Iranian Journal of Science and Technology, Transactions of Electrical<br>Engineering   |  |
|                         | 3- Hojjat Babaei Kia ," A Low Noise Amplifier Design Based On Noise Canceling<br>Technique For Multistandard Wireless Applications", Circuits Syst Signal Process,<br>springer.  |  |
| Conference<br>Paper     | 1- Hojjat Babaei Kia , "Adaptive CMOS LNA Using Highly Tunable Active  |  |
|                         | Inductor", ICEE2014, Tehran  |  |
|                         | 2- Hojjat Babaei Kia, " A High Tuning Range Voltage Controlled Oscillator Based on Differential Active Inductor", ICEEE2014, Gonabad   |  |
|                         | 3- Hojjat Babaei Kia, "A High Gain and Low Noise Amplifier Design Using<br>New Noise Canceling Technique For Wireless Receivers", The Third<br>International Conference on applied research in electrical engineering, EMME,<br>TEHRAN,IRAN, 2016.   |  |
|                         | 4- Hojjat Babaei Kia, " A 2.4 GHz Low Power Narrowband Differential LNA using Differential Active Inductor", National Conference on Computer, Electrical, Mechanical, Mechatronics", www. ecme.ir, TEHRAN, IRAN, 2016.   |  |
|                         | 5- Hojjat Babaei Kia, "Design of A Robust and Narrowband Low Noise Amplifier<br>Utilizing Noise Reduction Method For wireless Receivers and Transceivers", International<br>Conference on Electrical Engineering", www. EE2016.ir, TEHRAN, IRAN, 1 June 2016.  |  |
|                         | 6- Hojjat Babaei Kia, "A Noise Canceled Single-to-Differential Low Noise Amplifier<br>Using Differential Active Inductor", 2016 1st International Conference on New Research<br>Achievements in Electrical and Computer Engineering, AmirKabir university of<br>Technology, TEHRAN, IRAN, 13 May 2016. |  |
|                         | 7- Hojjat Babaei Kia " A Single-to-differential Low Noise Amplifier design Exploiting Differential Active inductor in 0.18µm CMOS Technology ", The International Conference On New Research in Engineering Sciences, WWW.RKES.ir, University of tehran, Iran, 25,26 May 2016.                         |  |
| Patents                 | ثبت اختراعات   |  |
|                         | 1- Radio Frequency Active Inductor Circuit<br>(Patent IP Number: PT/4644/UTM/13)   |  |
|                         | 2- Active Inductor Circuit For Radio Frequency Applications<br>(Patent IP Number: PT/4646/UTM/13)  |  |
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3-Differential Active Inductor Circuit for Radio Frequency applications (Patent IP Number: PT/4645/UTM/13)